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SALES hereby certify that annexed is a true copy of the Provisional specification  
in connection with Application No. 2002951218 for a patent by COCHLEAR  
LIMITED as filed on 04 September 2002.



WITNESS my hand this  
Fifteenth day of September 2003

*J R Yabsley*

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SUPPORT AND SALES

**AUSTRALIA**

**Patents Act 1990**

**Cochlear Limited**

**PROVISIONAL SPECIFICATION**

*Invention Title:*

*Method and apparatus for measurement of evoked neural  
response*

The invention is described in the following statement:

**Field of the Invention**

The present invention relates to measurement of a neural response evoked by a stimulus, and in particular to a method and device for measuring the evoked neural response in the presence of stimulus artefacts.

5

**Background of the Invention**

Hearing loss, which may be due to many different causes, is generally of two types, conductive and sensorineural. Of these types, conductive hearing loss occurs where the normal mechanical pathways for sound to reach the hair  
10 cells in the cochlea are impeded, for example, by damage to the ossicles. Conductive hearing loss may often be helped by use of conventional hearing aid systems, which comprise a microphone and an amplifier for amplifying detected sounds so that acoustic information does reach the cochlea and the hair cells.

15 In many people who are profoundly deaf, the reason for deafness is sensorineural hearing loss, which is caused by an absence of, or destruction of, the hair cells in the cochlea which transduce acoustic signals into nerve impulses. These people are thus unable to derive suitable benefit from conventional hearing aid systems, no matter how loud the acoustic stimulus is  
20 made, because there is damage to or absence of the mechanism for nerve impulses to be generated from sound in the normal manner. It is for this purpose that cochlear implant systems have been developed. Such systems bypass the hair cells in the cochlea and directly deliver electrical stimulation to the auditory nerve fibres, thereby allowing the brain to perceive a hearing  
25 sensation resembling the natural hearing sensation normally delivered to the auditory nerve. US Patent 4,532,930, the contents of which are incorporated herein by reference, provides a description of one type of traditional cochlear implant system.

Cochlear implant systems have typically consisted of two essential  
30 components, an external component commonly referred to as a processor unit and an internal implanted component commonly referred to as a stimulator/receiver unit. Traditionally, both of these components have cooperated together to provide the sound sensation to a user.

The external component has traditionally consisted of a microphone for  
35 detecting sounds, such as speech and environmental sounds, a speech

processor that converts the detected sounds, particularly speech, into a coded signal, a power source such as a battery, and an external transmitter coil.

The coded signal output by the speech processor is transmitted transcutaneously to the implanted stimulator/receiver unit situated within a recess of the temporal bone of the user. This transcutaneous transmission occurs via the external transmitter coil which is positioned to communicate with an implanted receiver coil provided with the stimulator/receiver unit. This communication serves two essential purposes, firstly to transcutaneously transmit the coded sound signal and secondly to provide power to the implanted stimulator/receiver unit. Conventionally, this link has been in the form of an RF link, but other such links have been proposed and implemented with varying degrees of success.

The implanted stimulator/receiver unit traditionally includes a receiver coil that receives the coded signal and power from the external processor component, and a stimulator that processes the coded signal and outputs a stimulation signal to an intracochlea electrode assembly which applies the electrical stimulation directly to the auditory nerve producing a hearing sensation corresponding to the original detected sound.

As the implant is surgically implanted within the recipient, there is a need to obtain data about the actual performance of the electrode array following implantation as well as the response of the auditory nerve to stimulation. Such data collection enables detection and confirmation of the normal operation of the device, and allows the stimulation parameters to be optimised to suit the needs of the patient.

Typically, following the surgical implantation of the cochlear implant, the recipient must have the implant fitted or customised to conform with the specific recipient demands. This procedure collects and determines patient specific parameters such as threshold levels (T levels) and maximum comfort levels (C levels) for each stimulation channel. Essentially, this is manually performed by applying stimulation pulses for each channel and receiving an indication from the implant recipient as to the level and comfort of the resulting sound. For implants with a large number of channels for stimulation, this process is quite time consuming and rather subjective as it relies heavily on the recipient's subjective impression of the stimulation rather than any specific measurement. This aspect is further complicated in the case of children and prelingually or congenitally deaf patients who are unable to supply an accurate impression of

the resultant hearing sensation, and hence fitting of the implant may be sub-optimal. In such cases an incorrectly fitted implant may result in the recipient not receiving optimum benefit from the implant and in the cases of children may directly hamper the speech and hearing development of the child.

5 Therefore, as previously mentioned, there is a need to obtain objective measurements of patient specific data especially in cases where an accurate subjective measurement is not possible.

One proposed method of interrogating the performance of the implanted device and making objective measurements of patient specific data such as T  
10 and C levels is to directly measure the response of the auditory nerve to an electrical stimulus. The measurement of Electrically Evoked Compound Action Potentials (ECAPs) provides an objective measurement of the response of the nerves to electrical stimulus. Following electrical stimulation, the neural response is caused by the superposition of single neural responses at the  
15 outside of the axon membranes. The ECAP can then be measured in response to various stimulations and from this the performance of the implant can be assessed and patient parameters can be interpolated.

Indeed, there is a need to measure the response of nerves to electrical stimulation in many applications, and not just in the area of cochlear implants.  
20 The measurement of ECAPs has proven to provide a useful objective measurement in many such applications. By measuring the ECAP in response to a stimulation, the effectiveness of the stimulation can be assessed in relation to the neural response evoked by the stimulation.

A number of ECAP measurement methods and devices have been  
25 developed which attempt to measure the response of the nerves to electrical stimulus. In the area of cochlear implants where electrical stimulus is delivered to the nerve cells within the cochlea, such systems have essentially attempted to use the electrodes implanted within the cochlea to both deliver stimulation and to detect the responses of the nerves to such stimulation.

30 US Patent No. 5,758,651 describes one system and apparatus for recovering ECAP data from a cochlear implant. This system measures the neural response to the electrical stimulation by using the stimulus array to not only apply the stimulation but to also detect and receive the response. In this system the array used to stimulate and collect information is a standard intra-  
35 cochlear and/or extra-cochlear electrode array. Following the delivery of a stimulation pulse via chosen stimulus electrodes, all electrodes of the array are

open circuited for a period of time prior to and during measurement of the induced neural response. The purpose of open circuiting all electrodes during this period is to reduce the detected stimulus artefact measured with the ECAP nerve response.

5        Whilst prior art systems of this type have proven useful in capturing and investigating evoked neural responses in the cochlea, there are still a number of intrinsic limitations associated with such systems, which have affected the quality of the measurements of the neural response. In the main this has been due to the presence of stimulus artefacts in the measurement detected,  
10        resulting in a measurement being taken which is not necessarily a true indication of the actual ECAP response present.

      The process of distinguishing the actual ECAP from stimulus artefacts has presented considerable difficulties, including problems such as the fact that the signals that are to be measured are extremely low level signals (down to  
15        the order of 10  $\mu$ V). In cochlear implant applications in particular, an intracochlear electrode usually delivers a stimulus pulse with an amplitude typically in the range of 1V to 10V, which is many orders of magnitude greater than the ECAP response that is to be measured resulting from this stimulation.

      Providing for a system that is firstly able to deliver a stimulus of sufficient  
20        amplitude and also to detect the elicited response of the nerves to that particular stimulation has therefore been problematic. Due to the nature of the neural response, the sensing system must be ready to record this response within a short delay (preferably less than 50 $\mu$ s) after completion of the stimulus. In order to properly resolve the very small neural signal a large amplifier gain is  
25        required (typically of about 60dB to 70dB), however the neural signal is often superimposed on a much larger artefact which makes it difficult to extract the neural signal of interest due to the finite dynamic range of the amplifier and the need for high gain to resolve the signal.

      In the past, the only way useful measurements have been able to be  
30        obtained from the associated artefacts has been through the use of extensive post processing techniques. These techniques have attempted to apply complicated mathematical algorithms to the associated measurements in an attempt to cancel out the presence of the artefacts in the measurements. Such a system does not provide immediate results which can be acted upon, as the  
35        measured results often require time consuming analysis before they can be

used. With the need to use such results immediately to adjust patient T and C levels, existing methods are not satisfactory.

Similar needs exist in respect of measurement of neural responses evoked by other types of devices.

5 Any discussion of documents, acts, materials, devices, articles or the like which has been included in the present specification is solely for the purpose of providing a context for the present invention. It is not to be taken as an admission that any or all of these matters form part of the prior art base or were common general knowledge in the field relevant to the present invention before  
10 the priority date of each claim of this application.

Throughout this specification the word "comprise", or variations such as "comprises" or "comprising", will be understood to imply the inclusion of a stated element, integer or step, or group of elements, integers or steps, but not the exclusion of any other element, integer or step, or group of elements,  
15 integers or steps.

#### **Summary of the Invention**

According to a first aspect the present invention provides a method of measurement of an evoked neural response comprising the steps of:

20 obtaining a sensed signal representing the evoked neural response from a sensor;

passing the obtained sensed signal to a signal input of a high gain amplifier; and

25 altering a reference voltage of the high gain amplifier in order to prevent the high gain amplifier saturating with variations of the sensed signal.

By resetting or altering the reference voltage of the amplifier during measurement, the present invention enables a high gain amplifier to be used in obtaining measurements of an evoked neural response, thus enabling detection of smaller neural response signals than prior art measurement  
30 methods. While resetting the reference voltage of the amplifier during measurement causes the output of the amplifier to represent the amplified sensed signal in piecewise fashion, the piecewise output signal segments can nevertheless be relatively easily reconstructed into a continuous waveform.

The step of altering the reference voltage of the high gain amplifier is  
35 preferably performed by setting the reference voltage to be equal to a present value of the sensed signal. This can be simply achieved by a sample-and-hold

circuit having an input from the sensed signal. In such embodiments, a present dc-offset of the sensed signal is essentially removed, such that the high gain amplifier will only amplify and output variations of the sensed signal from the reference voltage. This enables such variations to be amplified by a relatively large amount, without the present dc-offset of the sensed signal causing the amplifier to enter saturation. It is to be appreciated that the present dc-offset is only "dc" when considered relative to the regularity with which the reference voltage of the high gain amplifier is altered.

Preferably, at the commencement of every sample period, the reference voltage of the high gain amplifier is altered to be equal to the present value of the sensed signal. In such embodiments of the invention, every sample obtained at the output of the high gain amplifier represents a change in the sensed signal which has occurred since the previous sample. In such embodiments, the amplifier is operating in a delta mode. With a sufficiently high sample rate, the change in the sensed signal during the duration of the sample period will be relatively small, enabling a larger gain to be applied to the signal without saturating the amplifier. Thus, even where a relatively large stimulus artefact contributes to the sensed signal and would otherwise cause saturation of the amplifier, the present invention enables high gain amplification of the sensed signal while avoiding saturation of the amplifier caused by large stimulus artefacts. That is, the method of the present invention assists in common mode rejection. In such embodiments of the present invention the obtained samples can simply be integrated or summed to obtain a continuous waveform representing the amplified sensed signal.

The step of obtaining may comprise obtaining a sensed signal of the neural response of an auditory nerve, obtained by one or more electrodes of an electrode array of a cochlear implant.

In accordance with a second aspect, the present invention provides a device for measuring an evoked neural response, the device comprising:

a sensor for obtaining a sensed signal representing the evoked neural response;

a high gain amplifier having a signal input for receiving the sensed signal, and having a reference input; and

means for altering a reference voltage at the reference input of the high gain amplifier in order to prevent the high gain amplifier saturating with variations of the sensed signal.



The device may comprise a cochlear implant. In such embodiments the sensor may comprise one or more electrodes of an electrode array of an implanted portion of the cochlear implant. The cochlear implant may be a totally implanted cochlear implant, as described in PCT/AU01/00769 by the present applicant, the contents of which are incorporated herein by reference. While the present invention is advantageous when used in typical cochlear implants having a relatively simple implanted portion and an external processor, embodiments of the present invention in which the device is a totally implanted cochlear implant may be of particular advantage, in that processing of the sensed signal is performed prior to transmission of the obtained samples over a transcutaneous RF link, thus reducing the impact of noise of the RF link on the accuracy of the sampled data.

The present invention may be particularly advantageous when used in conjunction with stimulus artefact cancellation schemes. For example, the present invention, when combined with the artefact cancellation technique disclosed in International Application No. PCT/AU02/00500 by the present applicant, the contents of which are incorporated herein by reference, may enable particularly accurate high resolution measurements of an evoked neural response to be obtained, despite the relatively small amplitude of the neural response and the presence of stimulus artefacts of significantly larger amplitude. For example, embodiments of the present invention may obtain samples of the amplified sensed signal from the output of the high gain amplifier, and as each sample is obtained, transmit that sample across a transcutaneous RF link such that processing of the samples can be performed externally. Alternatively, where the implanted portion has processing capability, internal processing may be performed and may avoid the effects of noise of the RF link on each sample.

#### **Brief Description of the Drawings**

Examples of the invention will now be described with reference to the accompanying drawings in which:

Figure 1 is a pictorial representation of a cochlear implant system within which the present invention may be implemented;

Figure 2 illustrates a typical evoked neural response;

Figure 3 illustrates stimulus voltages and saturation of an amplifier by a sensed signal, in a prior art system;

Figure 4 is a pictorial circuit representation of portions of the implanted component of the cochlear implant of Figure 1, in accordance with an embodiment of the present invention;

Figure 5 illustrates the input and output waveforms of a high gain amplifier in both normal mode and delta mode; and

Figure 6 is a circuit diagram of an amplifier stage for implementing delta mode.

### **Detailed Description of the Preferred Embodiments**

While the present invention is not directed solely to a cochlear implant, it is appropriate to briefly describe the construction of one type of known cochlear implant system with reference to Figure 1.

Known cochlear implants typically consist of two main components, an external component including a speech processor 29, and an internal component including an implanted receiver and stimulator unit 22. The external component includes a microphone 27. The speech processor 29 is, in this illustration, constructed and arranged so that it can fit behind the outer ear 11. Alternative versions may be worn elsewhere on the recipient's body. Attached to the speech processor 29 is a transmitter coil 24 that transmits electrical signals to the implanted unit 22 via a radio frequency (RF) link.

The implanted component includes a receiver coil 23 for receiving power and data from the transmitter coil 24. A cable 21 extends from the implanted receiver and stimulator unit 22 to the cochlea 12 and terminates in an electrode array 20, comprising, for example, twenty two intra-cochlear electrodes 25. One or more extra-cochlear electrodes 28 are also provided. The signals received via the RF link are applied by the array 20 to the basilar membrane 8 and the nerve cells within the cochlea 12 thereby stimulating the auditory nerve 9. The operation of such a device is described, for example, in US Patent No. 4,532,930. As depicted diagrammatically in Figure 1, the cochlear implant electrode array 20 has traditionally been inserted into the initial portion of the scala tympani of the cochlea 12 up to about a full turn within the cochlea.

A sound processor (not shown) of the external component 29 includes an amplifier and a speech processor that uses a coding strategy to extract speech from the sounds detected by the microphone 27. In the depicted embodiment, the speech processor of the cochlear implant can perform an audio spectral analysis of the acoustic signals and output channel amplitude

levels. The sound processor can also sort the outputs in order of magnitude, or flag the spectral maxima as used in the SPEAK strategy developed by Cochlear Ltd. Other coding strategies could be employed.

Figure 2 illustrates a typical evoked neural response, which may arise in response to a stimulus applied by the electrode array 20 as depicted in Figure 1. Period 70 in Figure 2 illustrates a stimulation period, during which a stimulus is applied to an auditory nerve. The neural response 72 typically commences approximately 100 microseconds after the onset of the stimulus phase 70, as indicated by period 71. The duration of the more significant features of the response is around 1000 microseconds, as indicated by period 73, while the response measurement period or window is usually around 1.5 to 3 milliseconds.

Figure 3 illustrates the problem which may occur in such systems where stimulus artefacts exist and where delta mode is not used. A stimulus applied by the electrode array comprises a negative pulse 35 followed by a positive pulse 36, each pulse having an amplitude of up to 10V. During time 36, a significant stimulus artefact remains in the vicinity of the auditory nerve, causing relatively large stimulus artefact voltages to exist on the electrodes 25 of the electrode array 20. An amplifier operating at relatively high gain in attempting to resolve the small neural response, will therefore be saturated by the stimulus artefacts in the sensed signal, as shown at 37. Consequently, no useful information can be obtained from the amplifier due to the stimulus artefact.

Figure 4 is a pictorial circuit representation of portions of the implanted component of the cochlear implant of Figure 1, in accordance with an embodiment of the present invention. When it is desired to measure a neural response evoked by application of a stimulus by the electrode array 20, a sensed signal is obtained by one or more of the intra-cochlear electrodes 25, which is often done with reference to a voltage on the extra-cochlear electrode 28. The sensed signal is passed to the signal input 31 of amplifier 30, and, in accordance with the present invention, the voltage applied to the reference input 32 of the amplifier is controlled throughout the measurement period. The output of the amplifier 31 is passed to coil 23 for transcutaneous transmission to an external coil for subsequent processing.

During measurement of the evoked neural response, the sensed signal from the electrode(s) 25, 28 is sampled at a high rate, which could be perhaps

20-50kHz. In the present embodiment, the amplifier 30 is operated in a delta mode, whereby at the commencement of each sample period, the voltage at the reference input of the amplifier is set to be equal to the sensed signal. In the present embodiment, the reference voltage is maintained by a sample-and-  
5 hold circuit (not shown), the input of which is taken from the sensed signal.

By operating the amplifier 30 in the delta mode, each sample measures only the change in the sensed signal which occurs during that sample period, and provides no information about the present dc-level of the sensed signal. Due to the high sampling rate, this change is relatively small, allowing the  
10 amplifier 30 to be operated with a high gain without saturating, and thus enabling higher resolution data to be extracted from the sensed signal. In the present embodiment, the sensed signal may be amplified by up to 75dB.

Figure 5 illustrates the input and output waveforms of a high gain amplifier operating in both normal mode and delta mode. The input signal  
15 shown in the upper voltage chart is sinusoidal for illustrative purposes and has a peak-to-peak amplitude of 200 microvolts. Of course, a neural response would be expected to differ from this waveform, but would typically have an amplitude of a similar order. During period 50, the amplifier operates in normal mode and amplifies the input signal with reference to zero volts. During this  
20 period, the output signal shown in the lower chart has a peak to peak amplitude of around 0.9 volts. During period 51 the amplifier operates in delta mode, whereby the input signal is amplified with reference to a reference voltage which changes approximately every 50 microseconds, that is, at around 20kHz. In this delta mode, the reference voltage is periodically changed to be equal to  
25 the present value of the input signal, such that over the ensuing 50 microseconds the amplifier only amplifies changes of the sensed signal from that value. Accordingly, the peak-to-peak amplitude of the output of the amplifier during period 51 is significantly smaller than during period 50. This allows a higher gain to be used when amplifying the input signal, while still  
30 avoiding saturation of the amplifier. Reconstruction of the output signal into a continuous waveform is a simple matter. Consequently, significantly more information may be extracted from the input signal by use of the delta mode and higher amplification.

Figure 6 is a circuit diagram of a fourth stage of a multi-stage amplifier  
35 for implementing delta mode. The multi-stage amplifier is used for amplifying the differential electrode voltage (the neural signals) to a single ended output.

It has very high selectable gain of 45, 55, 65 and 75db. The multi-stage amplifier inputs are connected to the electrodes. Stage 1 is a differential in and differential out amplifier with a gain of 15dB. Stage 2 is a differential in and single-ended output amplifier with a selectable gain of 15dB or 1.5dB. Stage 3  
 5 amplifies the signal from stage 2 by a selectable gain of 1 or 0.316.

Stage 4, shown in Figure 6, amplifies the signal from stage 3 by a gain of 30. Delta mode operation is implemented by this stage and can be applied selectively.

As stage 2 and stage 3 have alterable gain, the gain of the multi-stage  
 10 amplifier maybe selected to be 45dB, 55dB, 65dB or 75dB.

As it is an implanted item, the amplifier should possess low power consumption. The sensed signal from the evoked neural response is typically of the order of 100 microvolts, and is typically superimposed on a much larger signal, the stimulus artefact. Thus the amplifier requires large input common  
 15 mode rejection ratio, so that the stimulus artefacts, being the common mode input signals, can be largely cancelled out. Due to the large stimulus levels, the amplifier is typically held inactive until after the stimulus has concluded. However, due to the swift onset of the neural response, the amplifier must be able to be initialised very quickly after conclusion of the stimulus.

20 Further, as the stimulus artefact can have either a positive or negative slew depending on which electrodes are chosen to obtain the sensed signal, the amplifier must be capable of bipolar action.

The fourth stage amplifier, shown in Figure 6, provides delta mode capability. The amplifier is disabled when the en\_bar is set low. This stage  
 25 has an inverting amplifier with a gain of 30 obtained from the capacitor C50 and feedback capacitor C\_43. The sample and hold function is performed by the transmission gate (M150 & M153). When phi5 is set high, the signal is passed through. While it is at low, the signal is held. Phi4 resets the quiescent voltage of the output to the reference voltage of the amplifier, when it is low. The  
 30 reference voltage sets the quiescent output voltage. By altering the reference voltage of the amplifier, delta mode is enabled. When both Phi4 and Phi5 are high the voltage at the source of the transistor M127 is pulled to ground otherwise it is at its DC bias level. The differential pair and the gain transistor are never working at the same time. In this circuit either the differential pair are  
 35 setting the dc bias level of the output to its dc level and the input signal is not defined or else the differential pair are not being used and one output of it is

pulled to ground while the gain transistor M10 is operating. The differential pair sets the output to the reference level as follows. One side of the differential pair is connected to the reference, while the other side is connected to output. The active transistors M10, M145 act like the 2nd stage of OTA and there is  
5 unity gain feedback to close the loop. This ensures that output is set to ref when the differential pair is acting. Clamp4 goes high when the M126 starts to conduct. That is when the output voltage is one threshold lower than the input. The overload operates in one direction only. An overload (clamp4) is not truly necessary as the true amplifier output can itself be an indication of overload but  
10 clamp4 again is there for completeness.

The voltage chart of Figure 5 shows the multi-stage amplifier input voltage and the corresponding output voltage. With such a circuit, a common mode rejection ratio of perhaps 40dB may be achieved, and delta mode operation may be implemented.

15 While an embodiment has been described in which the reference voltage of the amplifier is altered at the commencement of every sample period, it is to be appreciated that in alternate embodiments of the present invention the reference voltage may be altered with more or less frequency and still provide the advantages of the present invention. Such embodiments are thus within  
20 the scope of the present invention.

Further, it is to be appreciated that the present invention may be used to advantage in conjunction with the neural response measurement method disclosed in International Application No. PCT/AU02/00500 by the present applicant. Such a stimulus artefact cancellation scheme may further enhance  
25 measurements which may be obtained by the system and method of the present invention.

Additionally, the present invention may also be used to particular advantage where the implanted portion of the prosthesis has processing capability, such as the system disclosed PCT/AU01/00769 by the present  
30 applicant. In such a system, processing of the sensed signal may be carried out entirely within the implant, thus avoiding the deleterious effects caused by noise of an RF link. Preferably, the RF link is inactivated throughout such processing in order to minimise or entirely eliminate cross-talk and other such noise-effects on the data quality produced by such processing. Given that such  
35 internal processing is typically carried out on-board at perhaps 1.2V, very little cross-talk or electromagnetic interference will exist, thus enabling significantly

improved SNR to be obtained when processing the sensed signal. It is anticipated that such an improvement in the noise conditions may in fact enable measurements of brain stem responses reflected down the auditory nerve to be measured, in addition to measurement of the evoked neural response itself.

5        Such internal processing may comprise repeatedly applying a stimulus and measuring the evoked neural response, and averaging the measured responses. For example, 100 repetitions of applying the stimulus and measuring the response may be carried out in order to produce an averaged response. Once the averaged response is obtained, a transcutaneous RF link  
10       may then be activated and the averaged response transmitted externally for analysis or subsequent processing. The internal processing may alternatively be carried out as disclosed in International Application No. PCT/AU02/00500 by the present applicant in respect of an external processing procedure.

15       It will be appreciated by persons skilled in the art that numerous variations and/or modifications may be made to the invention as shown in the specific embodiments without departing from the spirit or scope of the invention as broadly described. The present embodiments are, therefore, to be considered in all respects as illustrative and not restrictive.

Dated this fourth day of September 2002

**COCHLEAR LIMITED**

Patent Attorneys for the Applicant:

**F B RICE & CO**

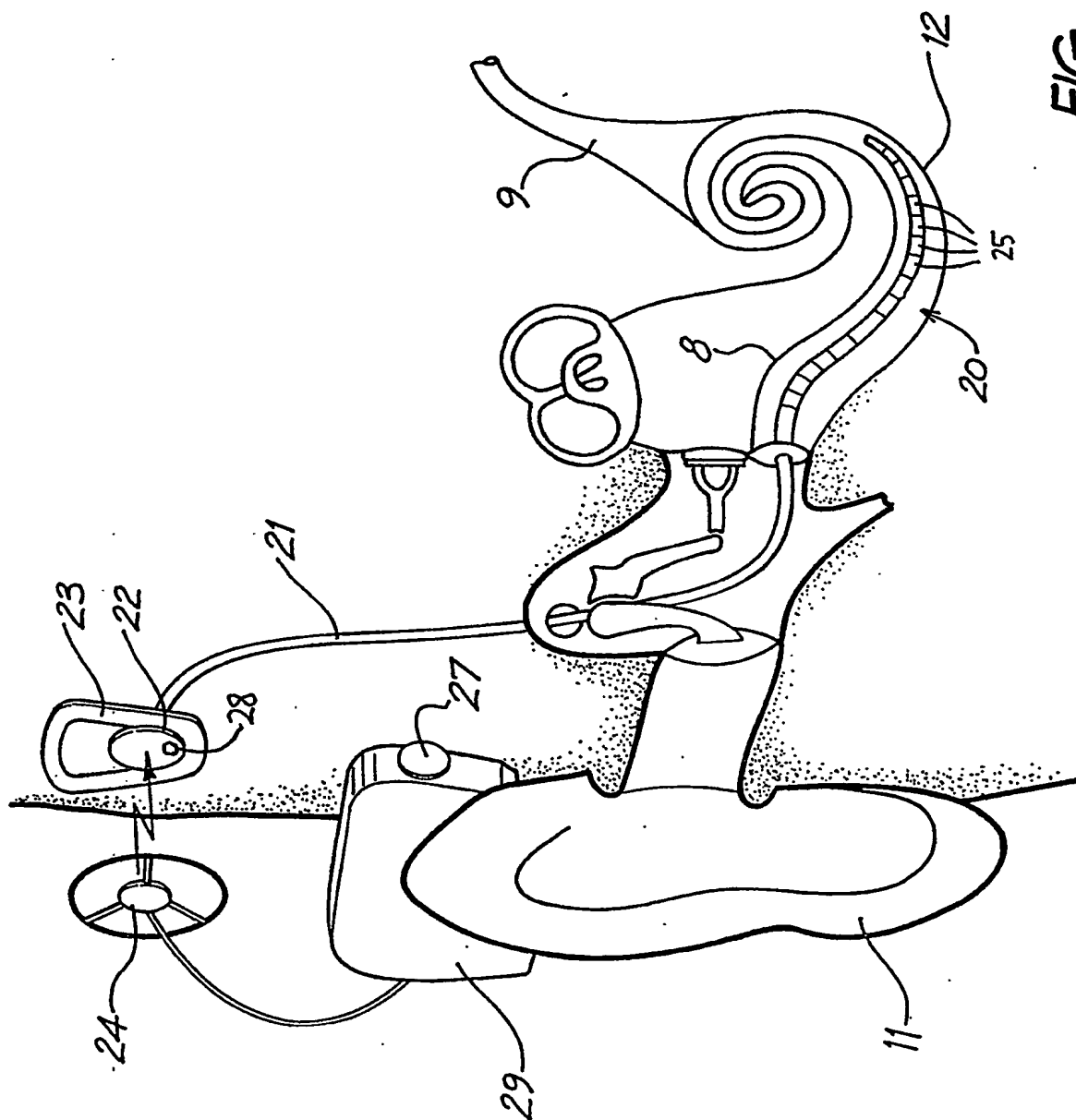


FIG. 1



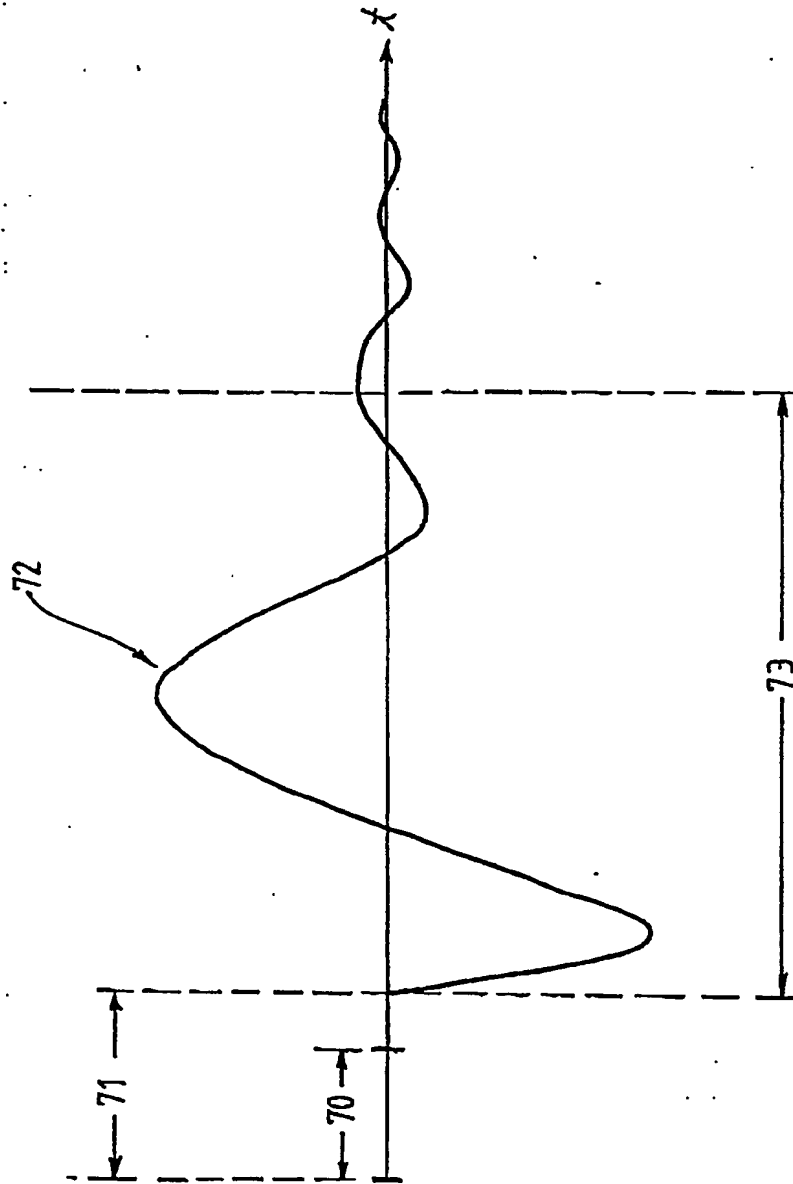


FIGURE 2

3/5

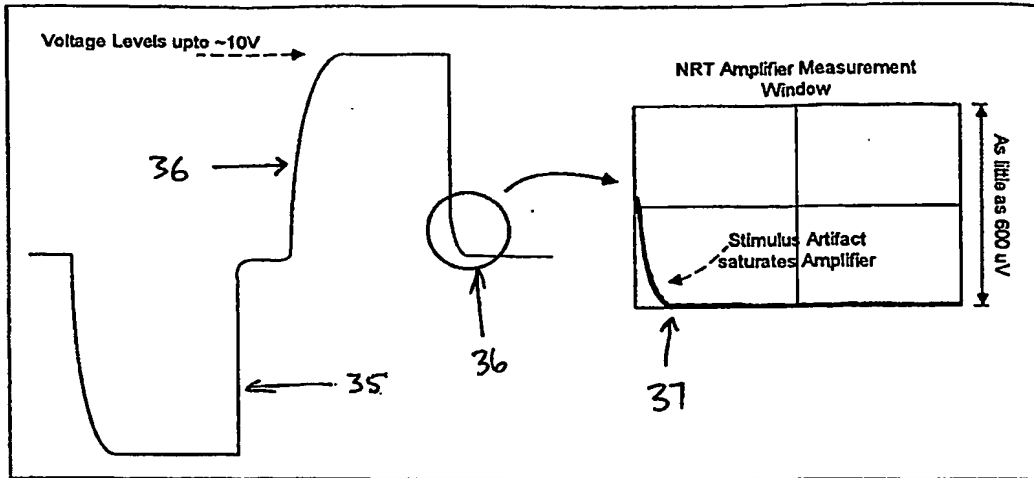


FIGURE 3

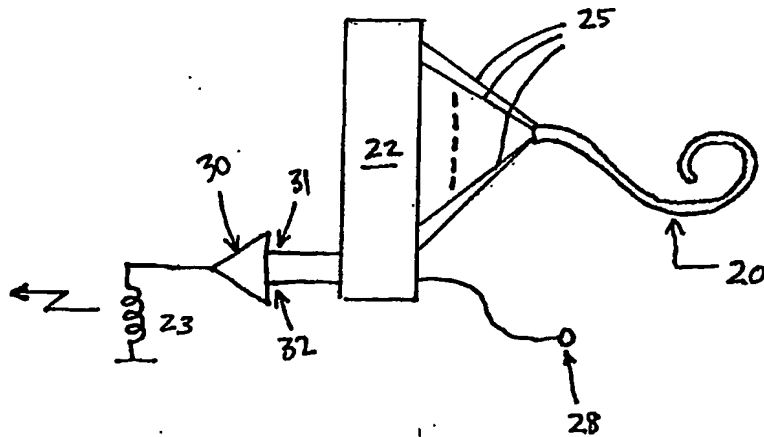
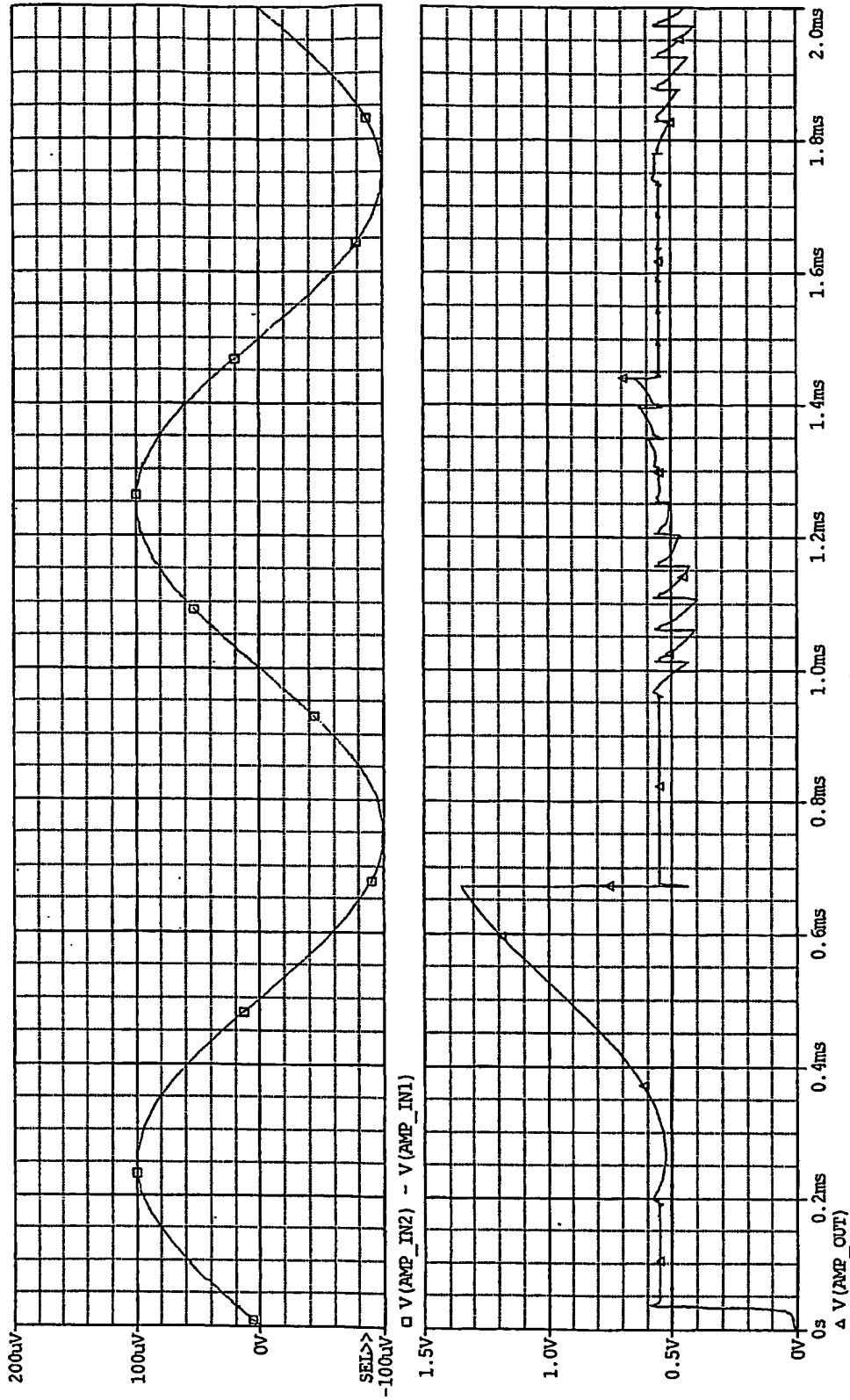


FIGURE 4

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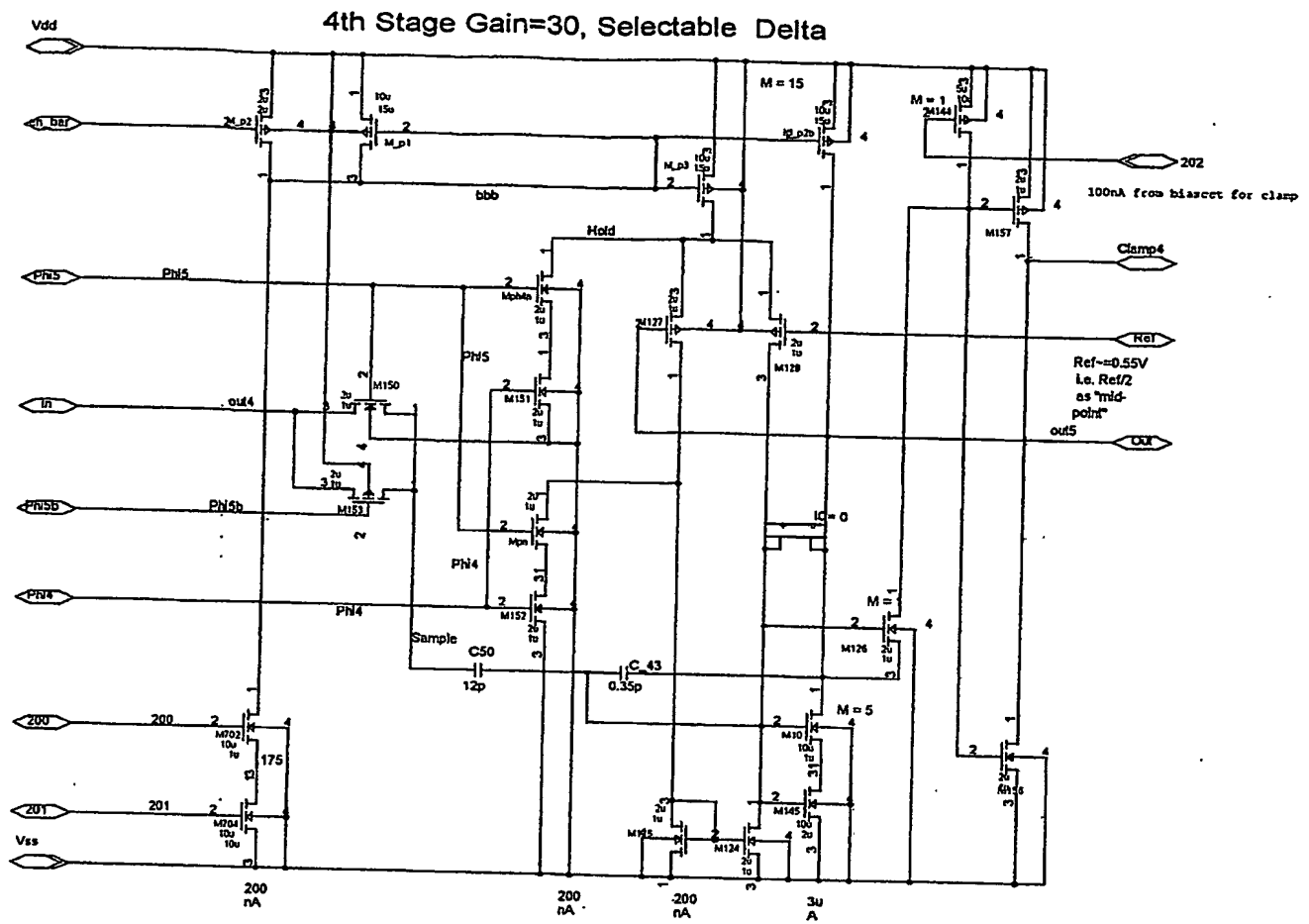


Time

Figure 5

50

51



Note: For 1st and 2nd stage, all cap are drawn on top of Vdd-connected N-well

**Figure 6**